

## (Reference No. 5) Contention Patterns

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SH4 Program (Read) G2 Bus Control Register, modem, G2 Bus Expansion Area								Con3		
SH4 Program (Write) G2 Bus Control Register, modem, G2 Bus Expansion Area			Con2					Con3		
SH4 Program (Read) AICA Register, AICA Sound Memory		Con2		Con2		Con2	Con4	Con3	Con5	Con5
SH4 Program (Write) AICA Register, AICA Sound Memory			Con2	Con1				Con3	Con6	
SH4 DMA (ch1, ch3) G2 Bus Expansion Area (Source)								Con3		
SH4 DMA (ch1, ch3) G2 Bus Expansion Area (Destination)			Con2					Con3		
GD ? DMA (GD => G2 Bus Expansion Area)			Con4							
GD - DMA (GD => AICA Sound Memory)	Con3	Con3	Con3	Con3	Con3	Con3				
AICA - DMA (System Memory => AICA Sound Memory) (Texture Memory => AICA Sound Memory)			Con5	Con6						
EXT - DMA 1, 2 (System Memory => G2 Bus Expansion Area) (Texture Memory => G2 Bus Expansion Area) (G2 Bus Expansion Area => System Memory) (G2 Bus Expansion Area => Texture Memory)			Con5							